

Electronic Acknowledgement Receipt

EFS ID:	1059574
Application Number:	09849822
Confirmation Number:	8898
Title of Invention:	MICROPROCESSOR WITH SELECTIVELY OVERRIDING FIRST AND SECOND PREDICTIVE CALL/RETURN STACKS
First Named Inventor:	G. Glenn Henry
Correspondence Address:	James W. Huffman - 1832 N. Cascade Ave - Colorado Springs CO 80907 US 7194757103 Jim@huffmanlaw.net
Filer:	James Weldon Huffman/Taysie Locke
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Submitted with Payment	yes
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Information Disclosure Statement (IDS) Filed	CNTR_2050_IDS.pdf	39792	no	3
Warnings:					
Information:					
This is not an USPTO supplied IDS fillable form					
2	NPL Documents	Computer_Organization_and_Design.pdf	328842	no	3
Warnings:					
Information:					
3	NPL Documents	Computer_Organization_Design_pages_453-455.pdf	320273	no	4
Warnings:					
Information:					
4	NPL Documents	Online_Computing.pdf	95807	no	1
Warnings:					
Information:					
5	NPL Documents	The_Correlation_Branch.pdf	167985	no	3
Warnings:					
Information:					
6	NPL Documents	The_D_Latch.pdf	100404	no	1
Warnings:					
Information:					
7	NPL Documents	The_Impact_of_Delay.pdf	806756	no	10
Warnings:					
Information:					
8	Fee Worksheet (PTO-875)	fee-info.pdf	8220	no	2

Warnings:**Information:****Total Files Size (in bytes):**

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.